

D6FEC15ST  
Schottky Barrier Diodes  
150V, 6A

Feature

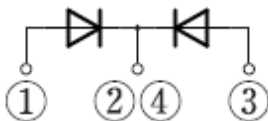
- SMD
- High Voltage
- Tj=175°C
- Ultra low I<sub>R</sub>
- Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FE  
Package (JEDEC Code): TO-252AB similar  
Package (JEITA Code): SC-63



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 175	°C
Junction temperature	Tj		-55 to 175	°C
Repetitive peak reverse voltage	V <sub>RRM</sub>		150	V
Average forward current	I <sub>F(AV)</sub>	50Hz sine wave, Resistance load, Rating for each diode IF(AV)/2, Tc=154°C ※	6	A
Surge forward current	I <sub>FSM</sub>	50Hz sine wave, Non-repetitive, 1cycle, Peak value, Tj=25°C	100	A
Surge forward current	I <sub>FSM1</sub>	tp=1ms, Sine wave, Non-repetitive, Peak value, Tj=25°C	180	A

※ :See the original Specifications

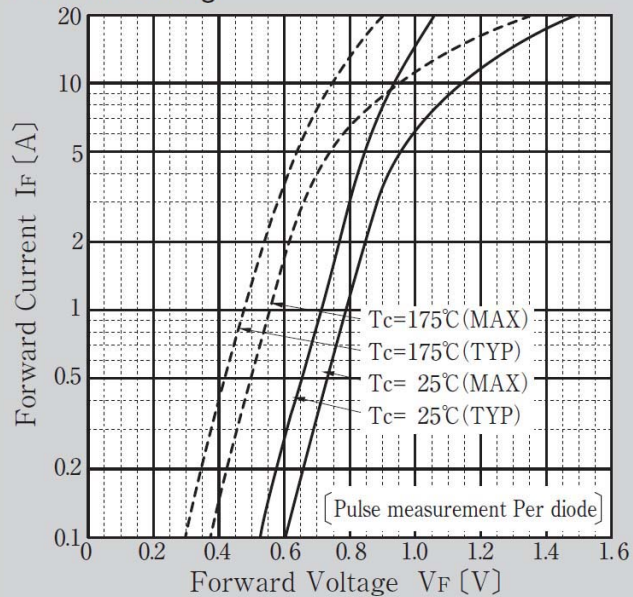
**Electrical Characteristics** (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	$V_F$	IF=3A, Pulse measurement, per diode			0.88	V
Reverse current	$I_R$	VR=150V, Pulse measurement, per diode			0.008	mA
Total capacitance	Ct	f=1MHz, VR=10V, per diode		52		pF
Thermal resistance	Rth(j-c)	Junction to case, With heatsink ※			4	°C/W

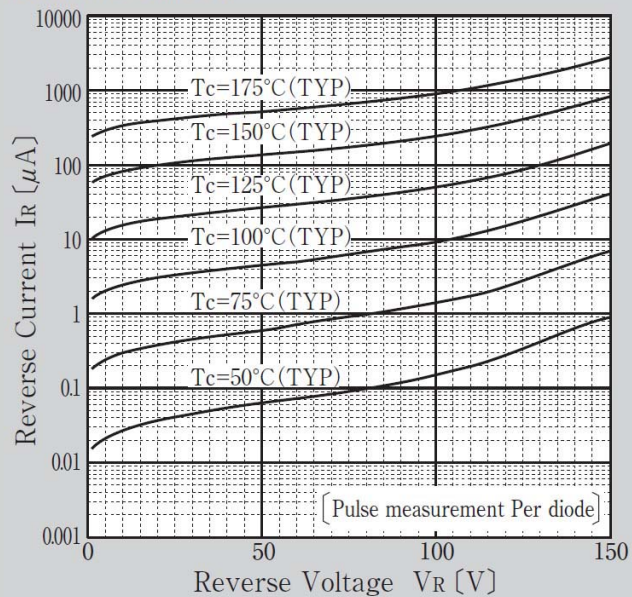
※ :See the original Specifications

## CHARACTERISTIC DIAGRAMS

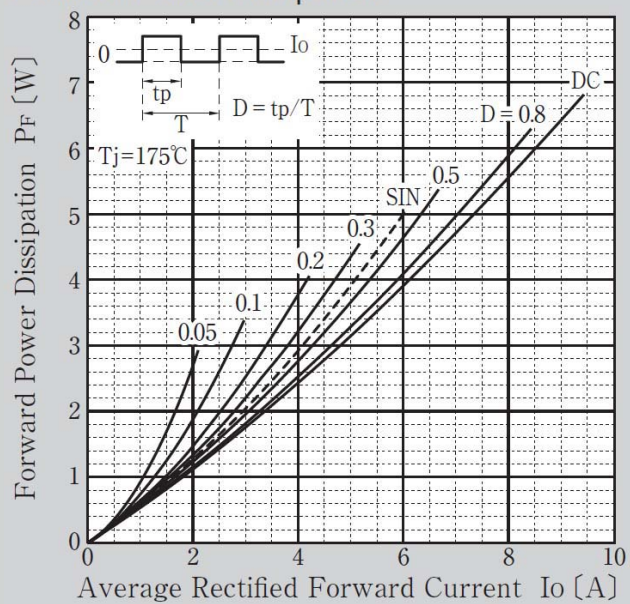
Forward Voltage



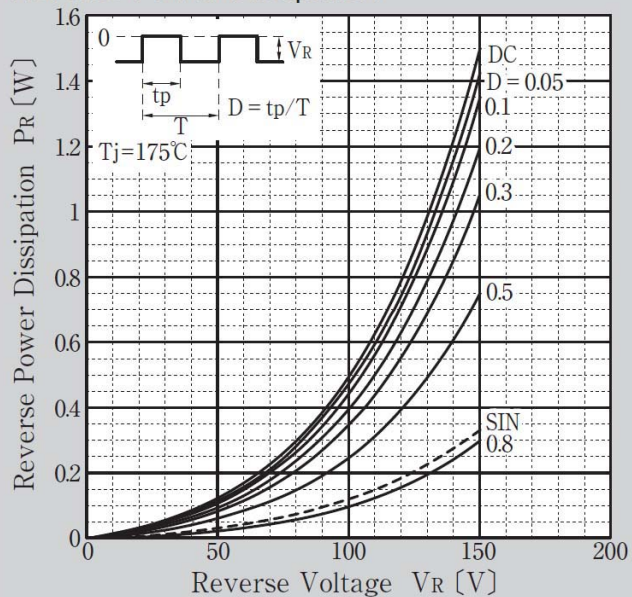
Reverse Current

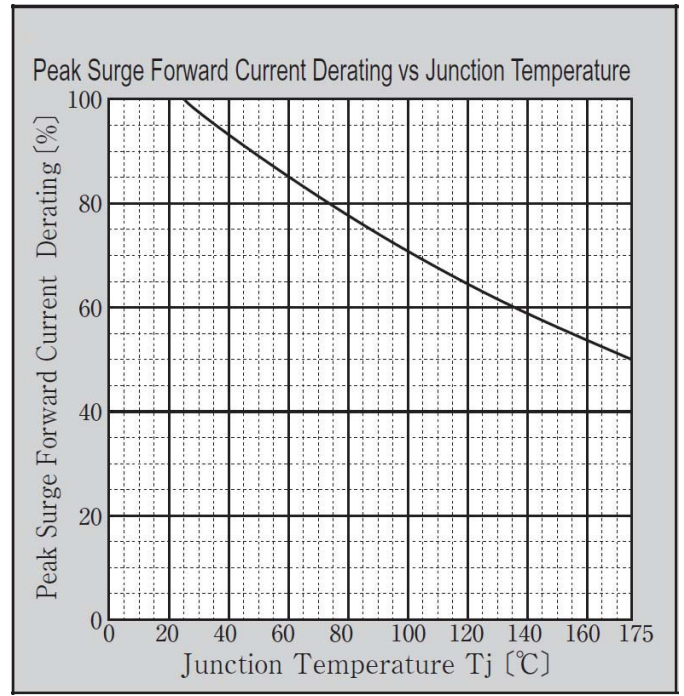
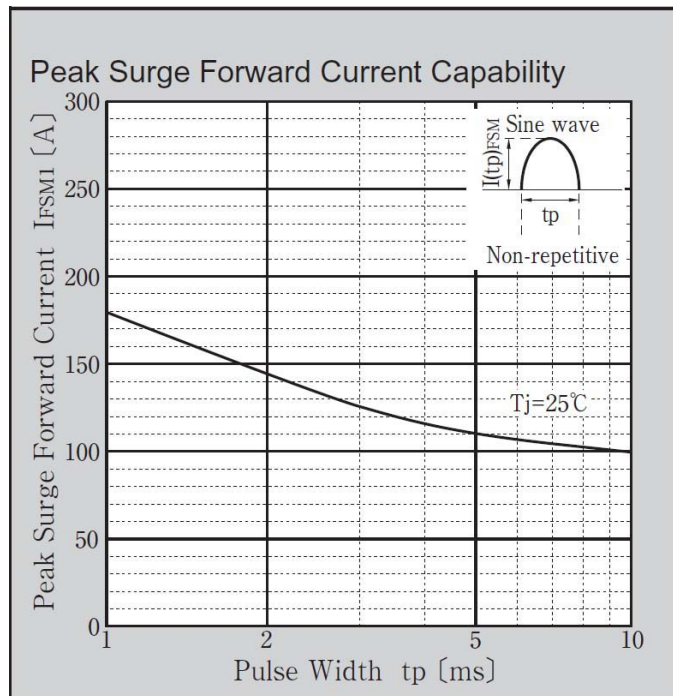
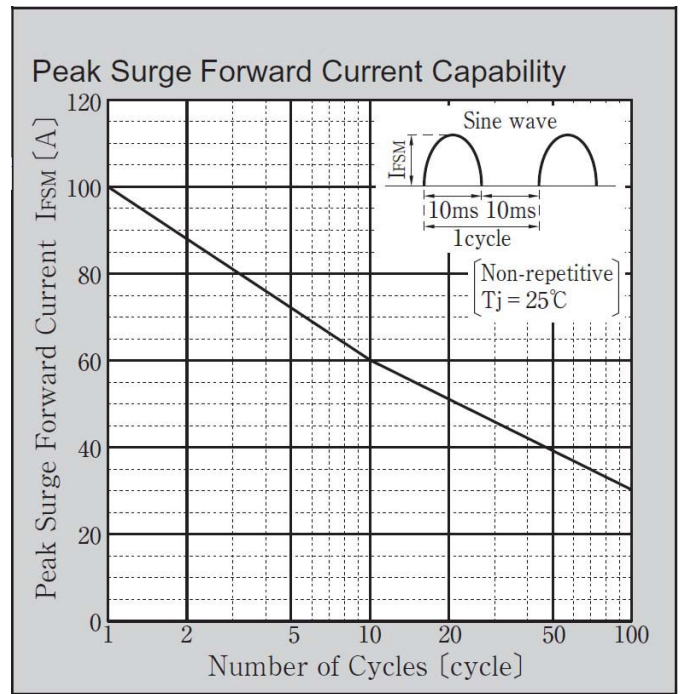
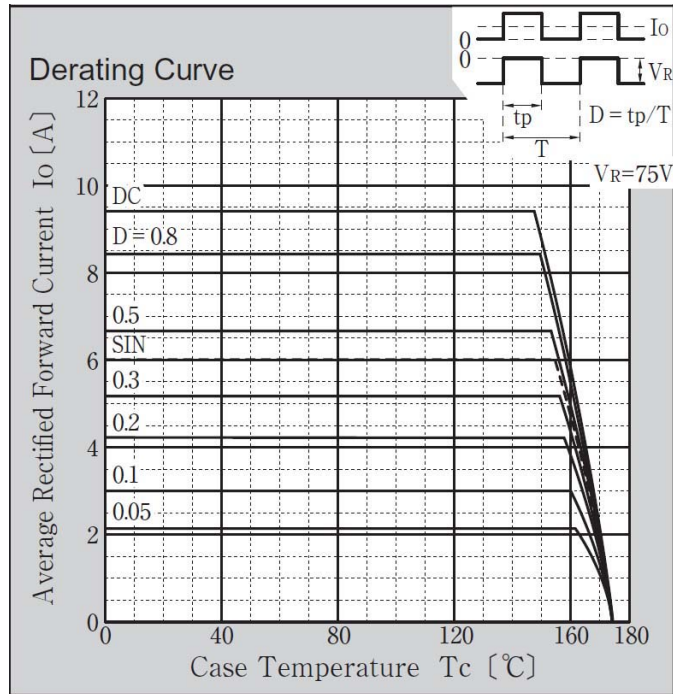


Forward Power Dissipation

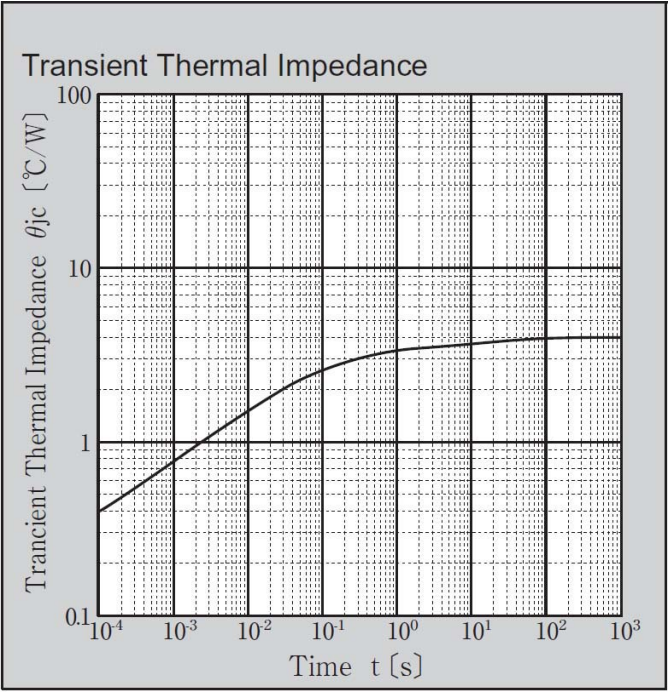
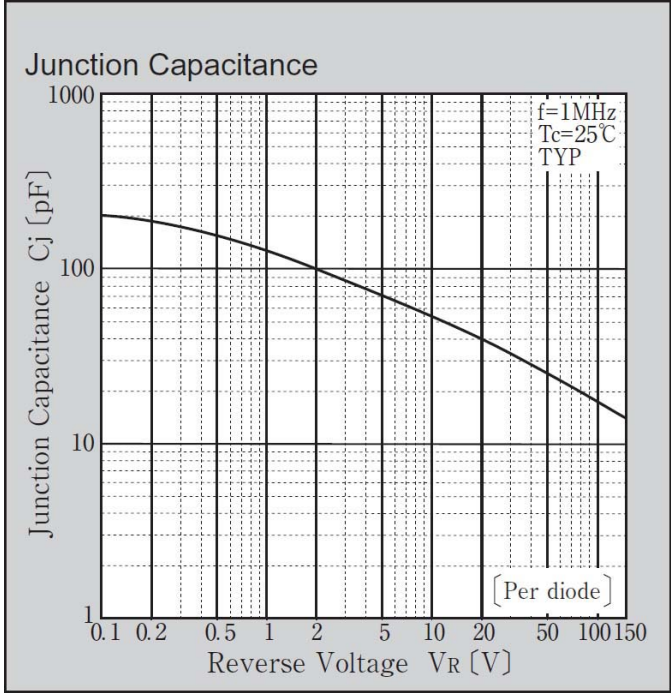


Reverse Power Dissipation



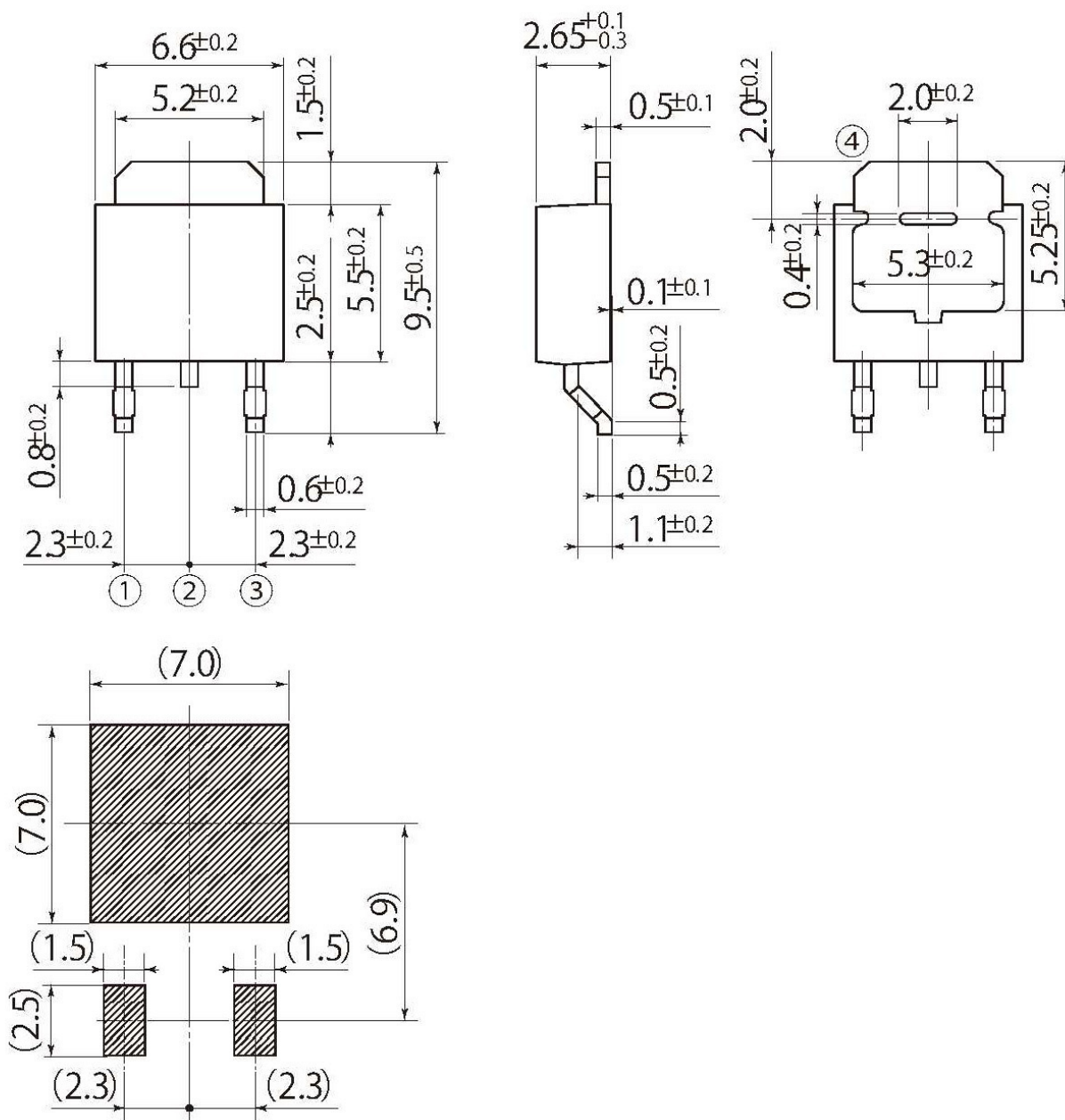






G3

JEDEC Code	TO-252AB similar
JEITA Code	SC-63
House Name	FE



Referential Soldering Pad

- Optimize soldering pad to the board design and soldering condition.

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