

D1FM3

Schottky Barrier Diodes 30V, 5A

Feature

- Small SMD
- · High Recovery Speed
- Low V_F
- Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): 1F
Package (JEDEC Code): DO-214AC





Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

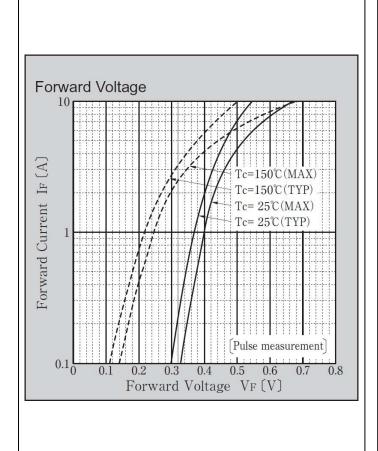
Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Junction temperature	Tj		-55 to 150	°C
Repetitive peak reverse voltage	V _{RRM}		30	V
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Tc=83°C **	5	Α
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C ※		Α
Surge forward current	I _{FSM}	50Hz sine wave, Non-repetitive, 1cycle, Peak value, Tj=25°C		Α

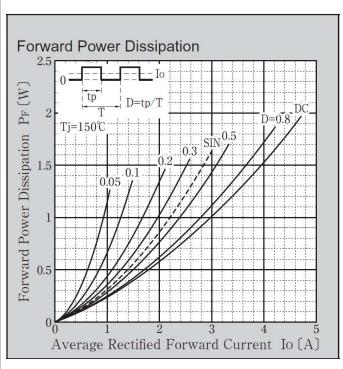
※ ∶See the original Specifications

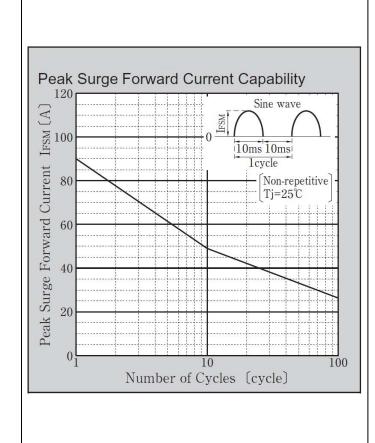
Electrical Characteristics (unless otherwise specified : Tc=25°C)

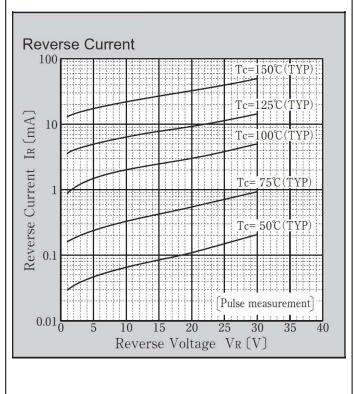
Item	Symbol	Conditions	Ratings			l lmit
			MIN	TYP	MAX	Unit
Forward voltage	V _F	IF=3.0A, Pulse measurement			0.46	V
Forward voltage	V _F	IF=1.0A, Pulse measurement			0.4	V
Reverse current	I _R	VR=30V, Pulse measurement			0.1	mA
Total capacitance	Ct	f=1MHz, VR=10V		130		pF
Thermal resistance	Rth(j-c)	Junction to case **			16	°C/W
Thermal resistance	Rth(j-l)	Junction to lead, On glass-epoxy substrate *			18	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On glass-epoxy substrate *			65	°C/W

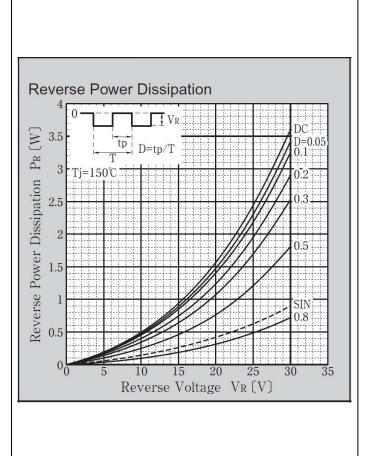
CHARACTERISTIC DIAGRAMS

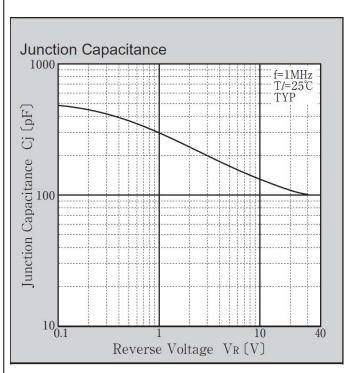


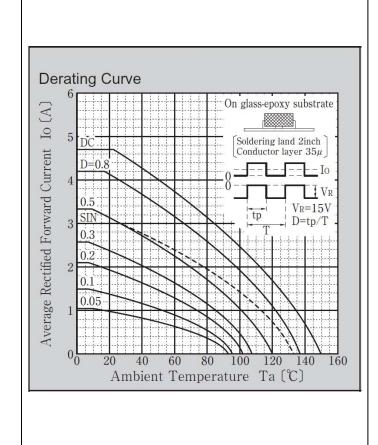


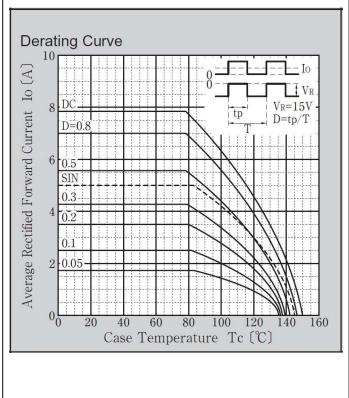


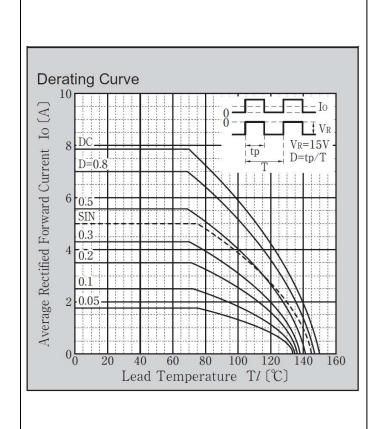


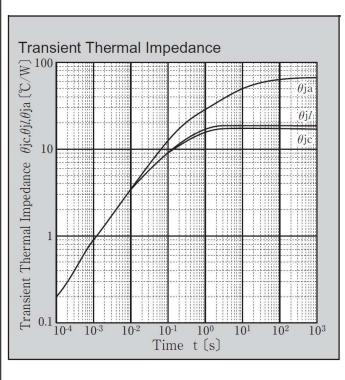










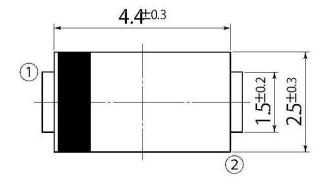


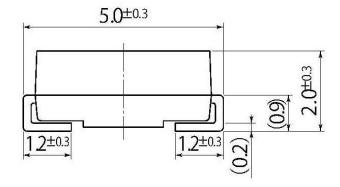
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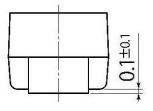
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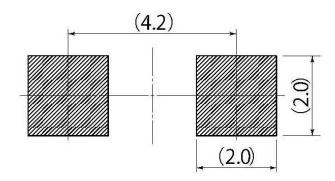
B3

JEDEC Code	DO-214AC	
JEITA Code	-	
House Name	1F, CF	









Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

Notes

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