

DE5L60U

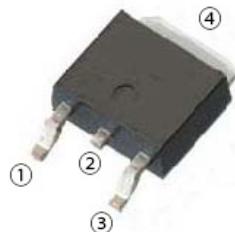
Fast Recovery Diodes
600V, 5A

Feature

- SMD
- High Voltage
- Low Noise
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): E-pack
Package (JEITA Code): SC-63



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : $T_c=25^\circ\text{C}$)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T_{stg}		-55 to 150	°C
Junction temperature	T_j		150	°C
Repetitive peak reverse voltage	V_{RRM}		600	V
Average forward current	$I_F(AV)$	50Hz sine wave, Resistance load, $T_c=91^\circ\text{C}$	5	A
Surge forward current	I_{FSM}	50Hz sine wave, Non-repetitive 1 cycle, Peak value, $T_j=25^\circ\text{C}$	60	A

※ : See the original Specifications

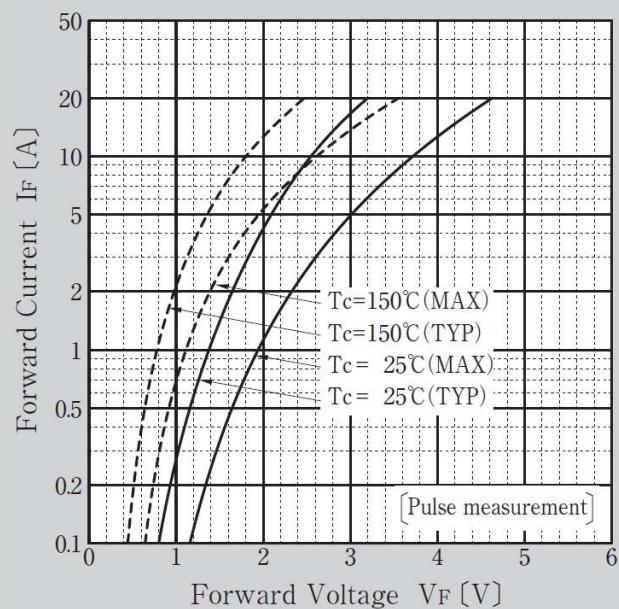
Electrical Characteristics (unless otherwise specified : $T_c=25^\circ\text{C}$)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	V_F	$I_F=5.0\text{A}$, Pulse measurement			3	V
Reverse current	I_R	$VR=600\text{V}$, Pulse measurement			25	μA
Reverse recovery time	trr	$IF=0.5\text{A}$, $IR=1.0\text{A}$			25	ns
Total capacitance	C_t	$f=1\text{MHz}$, $VR=10\text{V}$		30		pF
Thermal resistance	$R_{th(j-c)}$	Junction to case			4	°C/W

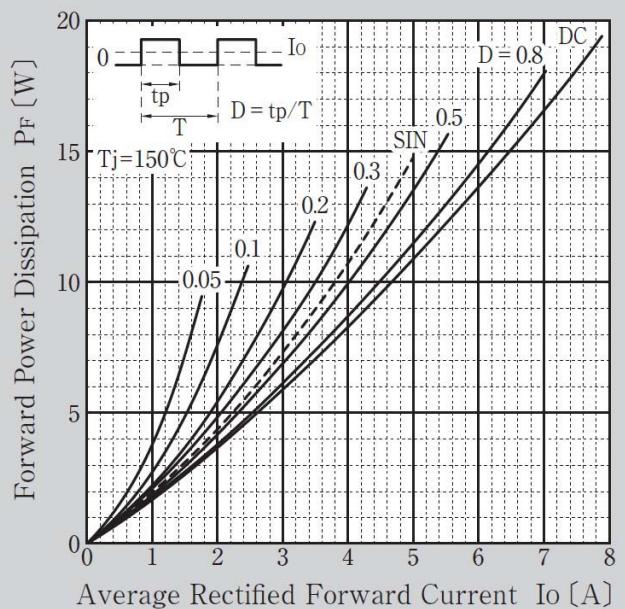
※ : See the original Specifications

CHARACTERISTIC DIAGRAMS

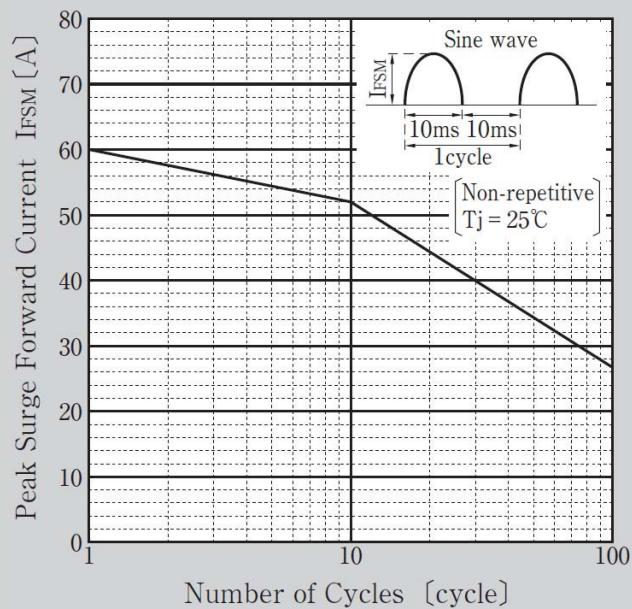
Forward Voltage



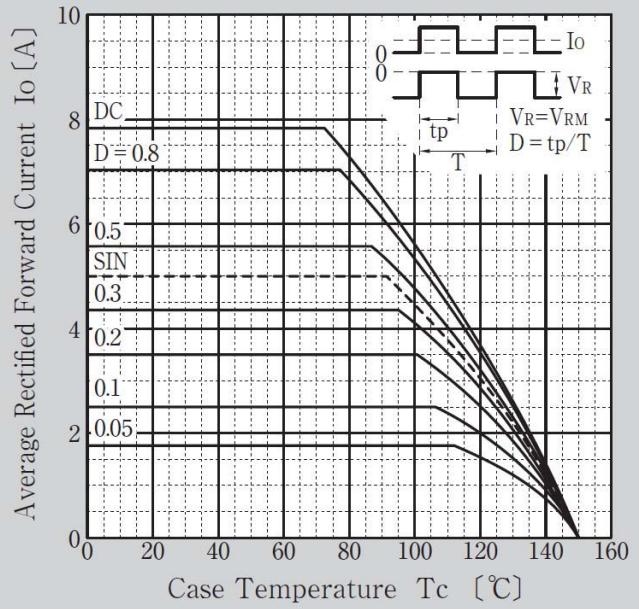
Forward Power Dissipation

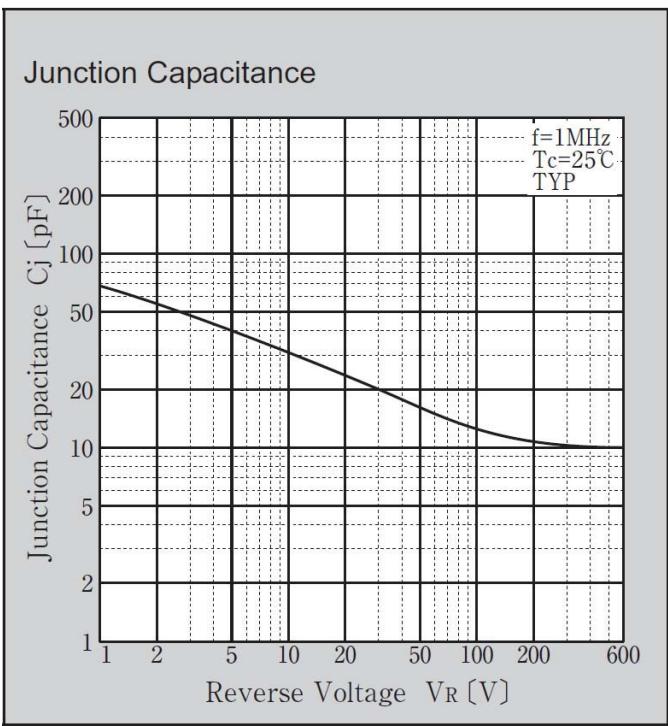


Peak Surge Forward Current Capability



Derating Curve





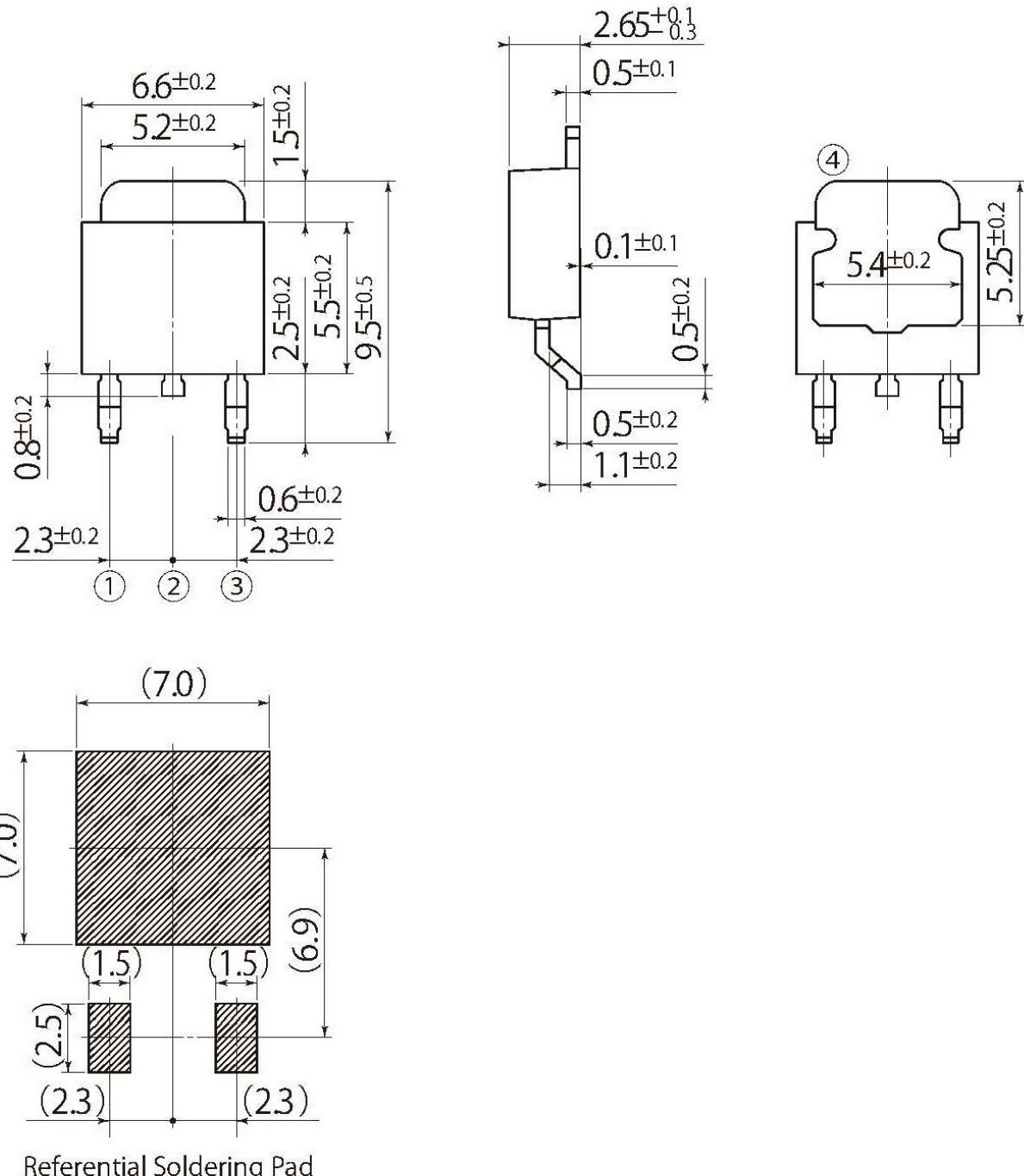
Outline Dimensions

unit:mm

scale: 3/1

G1

JEDEC Code	-
JEITA Code	SC-63
House Name	E-pack



- Optimize soldering pad to the board design and soldering condition.

Notes

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