

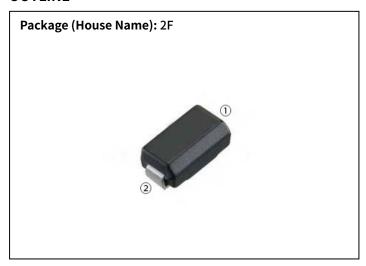
D5FE60

General Rectifying Diodes 600V, 5.00A

Feature

- Small SMD
- · High ESD Capability
- Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified: Tl=25°C)

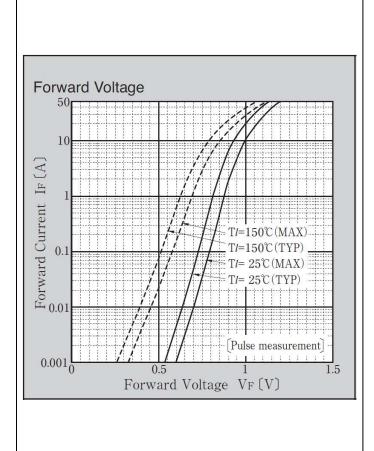
Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	T _{stg}		-55 to 150	°C
Junction temperature	T _j		-55 to 150	°C
Repetitive peak reverse voltage	V _{RRM}		600	V
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, Tl=82°C	5	Α
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, Tl=90°C	4.45	Α
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, On alumina substrate, Ta=25°C ※	2.02	А
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C ※	1.44	А
Surge forward current	I _{FSM}	50Hz sine wave, Non-repetitive 1 cycle peak value, Tj=25°C	300	А
Surge forward current	I _{FSM1}	tp=1ms, sine wave, Non-repetitive, peak value, Tj=25°C	720	А

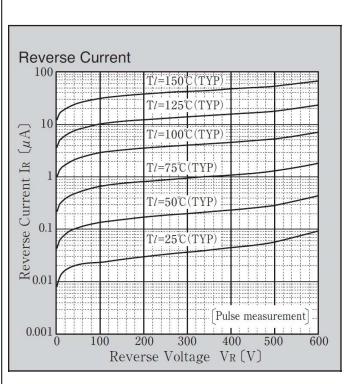
^{* :} See the original Specifications

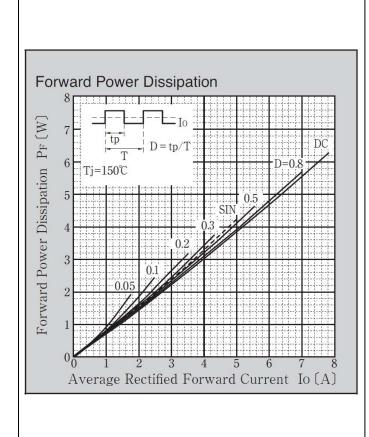
Electrical Characteristics (unless otherwise specified : Tl=25°C)

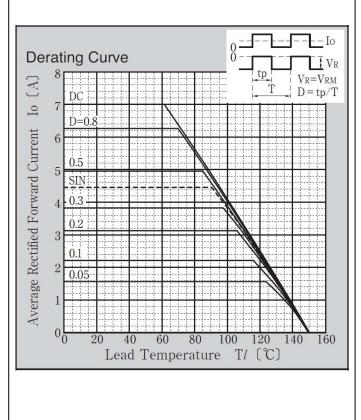
Item	Symbol	Conditions	Ratings			11
			MIN	TYP	MAX	Unit
Forward voltage	V_{F}	IF=5.0A, Pulse measurement			0.95	٧
Reverse current	I _R	VR=600V, Pulse measurement			10	μΑ
Electro static dischange Capability	V _{ESD}	C=330pF, R=330Ω, Polarity±, Aerial discharge		25		kV
Thermal resistance	$R_{th(j-l)}$	Junction to lead			16	°C/W
Thermal resistance	R _{th(j-a)}	Junction to ambient, On alumina substrate *			80	°C/W
Thermal resistance	$R_{th(j-a)}$	Junction to ambient, On glass-epoxy substrate *			115	°C/W

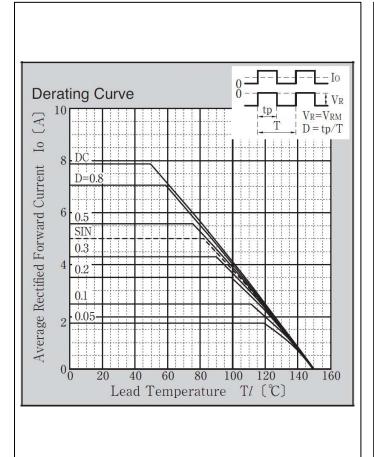
CHARACTERISTIC DIAGRAMS

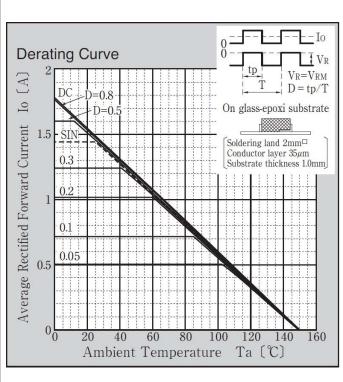


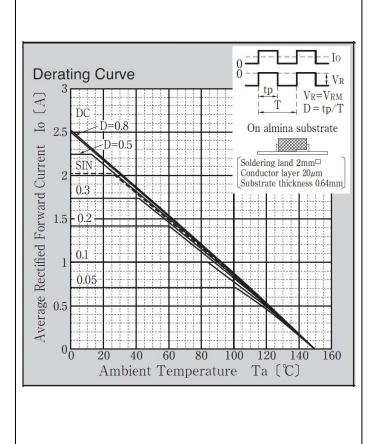


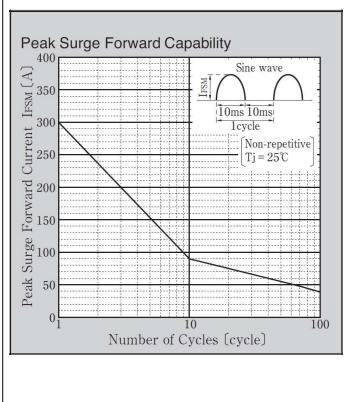


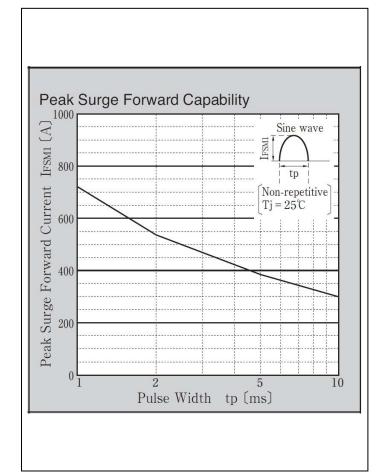


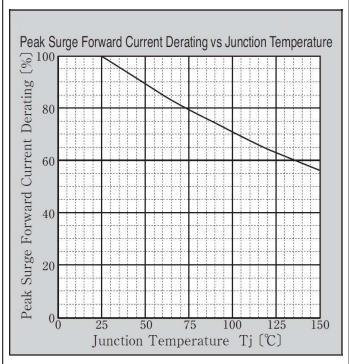


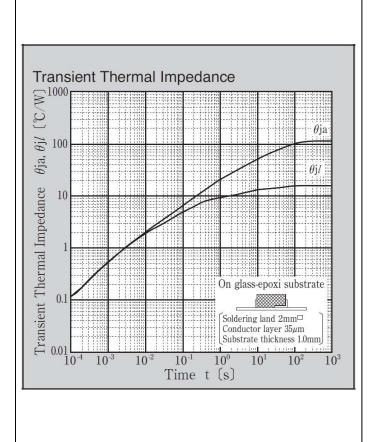


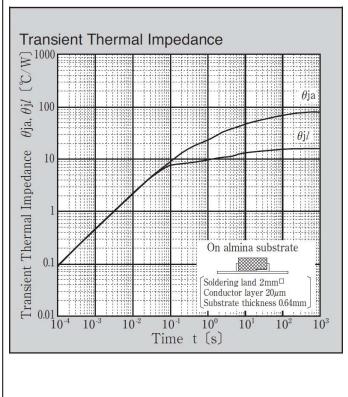






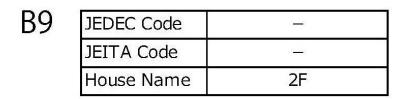


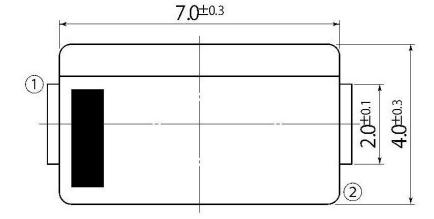


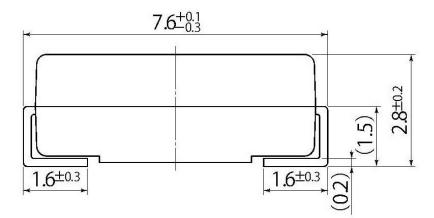


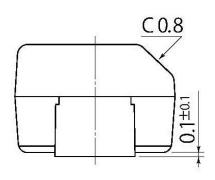
unit:mm

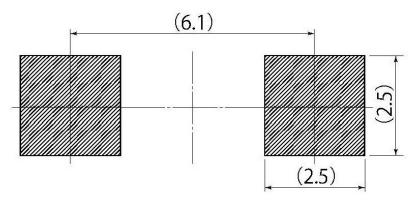
scale: 10/1











Referential Soldering Pad

[•] Optimize soldering pad to the board design and soldering condition.

Notes

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[Specific applications]

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